

**UNIVERSITY OF KERALA**

(Abstract)

B.Tech (Part-time Re-structured Scheme 2013) Degree Course - Scheme - Modified - Orders issued.

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**ACADEMIC A.III .SECTION**

No.Ac.A.III/4/FoE&T/IV-B-6-2/2016

Dated, Thiruvananthapuram: 15/06/2016

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- Read
1. U.O.No.Ac.AIII/4/FoE&T/IV H(2)/2014 dated 13/08/2014.
  2. Minutes of the annual meeting of the Boards of Studies in Engineering (Pass I, II & III) held on 11.01.2016.
  3. Minutes of the annual meeting of the Faculty of Engineering and Technology held on 05.03.2016. (IV-B-6-2)
  4. Minutes of the meeting of the Academic Council held on 18<sup>th</sup> April 2016.

**ORDER**

The Regulations and Scheme of B.Tech (Part-time Re-structured Scheme 2013) Degree Course were approved by the University vide U.O. read (1) above.

The Boards of Studies in Engineering (Pass I, II & III) vide paper read (2) above recommended to modify the Scheme of B.Tech (Part-time Restructured Scheme 2013) Degree Course in Computer Science and Engineering incorporating the changes effected in the Scheme of VII and VIII semesters of regular B.Tech Degree Course in Computer Science and Engineering.

The Faculty of Engineering and Technology vide paper read (3) above approved the modified Scheme as recommended by the Boards of Studies in Engineering (Pass I, II & III). The Academic Council vide paper read (4) above approved the same.

Orders are issued accordingly. (Modified Scheme appended)

Sd/-

**SOBHANAKUMARI.K**  
DEPUTY REGISTRAR(Acad.II)  
For **REGISTRAR**

To

1. The Dean, Faculty of Engineering and Technology.
2. The Chairmen, Boards of Studies in Engineering (Pass I, II & III).
3. The Principals of CET and TKMCE, Kollam.
4. PA to Registrar/CE.
5. DR(Engg)/AR (Engg)
6. The Director, KUCC.
7. EE II D Section/ EB IV Section
8. Stock File/File Copy.

Forwarded /By Order,

**SECTION OFFICER**

## Modified Scheme

### Fourth Semester

PT B. Tech Course No	Equivalent B. Tech Course No	Name of subject	Credits	Weekly load, hours			C A Marks	Exam Duration Hrs	U E Max Marks	Total Marks
				L	T	D/ P				
P 13.401	13.601	Compiler Design ( FR)	4	3	1	-	50	3	100	150
P 13.402	13.602	Principles of Programming Languages ( R)	3	2	1	-	50	3	100	150
P 13.403	13.603	Design and Analysis of Algorithms (FR)	4	3	1	-	50	3	100	150
P 13.404	13.604	Computer Networks ( FR)	3	2	1	-	50	3	100	150
P 13.405	13.406	Formal Languages and Automata Theory ( R)	4	3	1	-	50	3	100	150
P 13.406	13.408	Digital System Lab( R)	4	-	-	4	50	3	100	150
<b>Total</b>			<b>22</b>	<b>13</b>	<b>5</b>	<b>4</b>	<b>300</b>		<b>600</b>	<b>900</b>

### Fifth Semester

PT B. Tech Course No	Equivalent B. Tech Course No	Name of subject	Credits	Weekly load, hours			C A Marks	Exam Duration Hrs	U E Max Marks	Total Marks
				L	T	D/ P				
P 13.501	13.503	Operating Systems ( FR)	3	2	1	-	50	3	100	150
P 13.502	13.504	Systems Programming ( FR)	3	2	1	-	50	3	100	150
P 13.503	13.506	Object Oriented Design and JAVA Programming( R)	3	2	1	-	50	3	100	150
P 13.504	13.703	Embedded Systems ( R)	4	3	1	-	50	3	100	150
P 13.505	13.507	Object Oriented Programming Lab ( R)	4	-	-	4	50	3	100	150
P 13.506	13.508	Application Software Development Lab ( R)	4	-	-	4	50	3	100	150
<b>Total</b>			<b>21</b>	<b>9</b>	<b>4</b>	<b>8</b>	<b>300</b>		<b>600</b>	<b>900</b>

### Sixth Semester

PT B. Tech Course No	Equivalent B. Tech Course No	Name of subject	Credits	Weekly load, hours			C A Marks	Exam Duration Hrs	U E Max Marks	Total Marks
				L	T	D/ P				
P 13.601	13.605	PC Hardware and Interfacing ( R)	4	3	1	-	50	3	100	150
P 13.602	13.606	Signals and Systems ( R)	3	2	1	-	50	3	100	150
P 13.603	13.801	Cryptography and Network Security ( R)	4	3	1	-	50	3	100	150
P 13.604	13.802	Computer System Architecture ( R)	4	3	1	-	50	3	100	150
P 13.605	13.607	Microprocessor Lab ( R)	4	-	-	4	50	3	100	150
P 13.606	13.608	System Software Lab ( R)	4	-	-	4	50	3	100	150
<b>Total</b>			<b>23</b>	<b>11</b>	<b>4</b>	<b>8</b>	<b>300</b>		<b>600</b>	<b>900</b>

### Seventh Semester

PT B. Tech Course No	Equivalent B. Tech Course No	Name of subject	Credits	Weekly load, hours			C A Marks	Exam Duration Hrs	U E Max Marks	Total Marks
				L	T	D/ P				
P 13.701	13.701	Computer Graphics ( R)	4	3	1	-	50	3	100	150
P 13.702	13.704	Software Engineering and Project Management ( FR)	3	2	1	-	50	3	100	150
P 13.703	13.705	Elective I	3	2	1	-	50	3	100	150
P 13.704	13.706	Elective II	3	2	1	-	50	3	100	150
P 13.705	13.707	Computer Hardware and Interfacing Lab ( R)	4	-	-	4	50	4	150	200
P 13.706	13.708	Operating Systems and Network Programming Lab ( R)	4	-	-	4	50	4	150	200
P 13.707	13.702	Seminar, Project Design and Industrial Visit (R)	4	-	-	4	150	-	-	150
<b>Total</b>			<b>25</b>	<b>9</b>	<b>4</b>	<b>12</b>	<b>450</b>		<b>700</b>	<b>1150</b>

## Eighth Semester

PT B. Tech Course No	Equivalent B. Tech Course No	Name of subject	Credits	Weekly load, hours			C A Marks	Exam Duration Hrs	U E Max Marks	Total Marks
				L	T	D/ P				
P 13.801	13.803	Distributed Systems ( R)	4	3	1	-	50	3	100	150
P 13.802	13.804	Elective III	4	3	1	-	50	3	100	150
P 13.803	13.805	Elective IV	4	3	1	-	50	3	100	150
P 13.804	13.806	Algorithm Design and Graphics Lab ( R)	4	-	-	4	50	4	150	200
P 13.805	13.807	Project Work and Viva Voce (FR)	5	-	-	5	150	-	100	250
<b>Total</b>			<b>21</b>	<b>9</b>	<b>3</b>	<b>9</b>	<b>350</b>		<b>550</b>	<b>900</b>