UNIVERSITY OF KERALA

(Abstract)

B.Tech Degree Course in Computer Science and Engineering (2013 Scheme) – Scheme of VII & VIII semesters - Modified - Orders issued.

= =

ACADEMIC A.III .SECTION

No.Ac.A.III/4/FoE&T/IV-B-4/2016   Dated, Thiruvananthapuram: 15/06/2016

= =

Read   1.   U.O.No.Ac.AIII/4/Engg/Tech/2013 dated 19/07/2013.
       2.   Minutes of the annual meeting of the Boards of Studies in Engineering (Pass I, II & III) held on 11.01.2016.
       3.   Minutes of the annual meeting of the Faculty of Engineering and Technology held on 05.03.2016. (IV-B-4)
       4.   Minutes of the meeting of the Academic Council held on 18th April 2016.

ORDER

The Regulations and Scheme of B.Tech Degree Course (2013 Scheme) were implemented vide University Order read (1) above.

The Boards of Studies in Engineering (Pass I, II & III) vide paper read (2) above recommended to approve the modified Scheme of VII & VIII semesters of B.Tech Degree Course in Computer Science and Engineering as recommended by the sub-committee. The Faculty of Engineering and Technology vide paper read (3) above approved the modified Scheme of VII & VIII semesters of B.Tech Degree Course in Computer Science and Engineering (2013 Scheme) as follows:

Seventh Semester

Course No	Name of subject	Credits	Weekly load, hours			C A Marks	Exam Duration Hrs	U E Max Marks	Total Marks
			L	T	D/P				
13.701	Computer Graphics (R)	4	3	1	-	50	3	100	150
13.702	Seminar, Project Design and Industrial Visit (R)	4	-	-	4	150	-	-	150
13.703	Embedded Systems (R)	4	3	1	-	50	3	100	150
13.704	Software Engineering and Project Management (FR)	3	2	1	-	50	3	100	150
13.705	Elective I	3	2	1	-	50	3	100	150
13.706	Elective II	3	2	1	-	50	3	100	150
13.707	Computer Hardware and Interfacing Lab (R)	4	-	-	4	50	4	150	200
13.708	Operating Systems and Network Programming Lab(R)	4	-	-	4	50	4	150	200
	Total	**29**	**12**	**5**	**12**	**500**		**800**	**1300**

Eighth Semester

Course No	Name of subject	Credits	Weekly load, hours			C A Marks	Exam Duration Hrs	U E Max Marks	Total Marks
			L	T	D/P				
13.801	Cryptography and Network Security (R)	4	3	1	-	50	3	100	150
13.802	Computer System Architecture (R)	4	3	1	-	50	3	100	150
13.803	Distributed Systems (R)	4	3	1	-	50	3	100	150
13.804	Elective III	4	3	1	-	50	3	100	150
13.805	Elective IV	4	3	1	-	50	3	100	150
13.806	Algorithm Design and Graphics Lab (R)	4	-	-	4	50	4	150	200
13.807	Project Work and Viva Voce (FR)	5	-	-	5	150	-	100	250
	Total	**29**	**15**	**5**	**9**	**450**		**700**	**1200**

The Academic Council vide paper read (4) above approved the same.

Orders are issued accordingly.

                 Sd/-
                 SOBHANAKUMARI.K
               DEPUTY REGISTRAR(Acad.II)
                    For **REGISTRAR**

To
1. The Dean, Faculty of Engineering and Technology.
2. The Chairmen, Boards of Studies in Engineering (Pass I, II & III).
3. PA to Registrar/CE.
4. DR(Engg)/AR (Engg)/ AR (ECL)
5. The Director, KUCC.
6. B.Tech Tabulation Sections/ EB IV Section
7. Stock File/File Copy.

Forwarded /By Order,

SECTION OFFICER